## REVISED HIGHER PHYSICS

## REVISION BOOKLET

## ELECTRONS AND ENERGY

Kinross High School

## Monitoring and measuring a.c.

## Alternating current:

Mains supply a.c.; batteries/cells supply d.c.
Electrons moving back and forth, in a circuit, many times per second is known as alternating current (a.c.)

Period: time taken to produce one complete wave, in seconds (s)

Frequency: number of waves produced every second, in hertz $(\mathrm{Hz})$. The frequency of the mains supply is 50 Hz .


## The oscilloscope:

If the oscilloscope displays an a.c. signal, then you can work out directly the peak voltage and the period. From the peak voltage, you can work out the root mean square; from the period, you can work out the frequency.

Y-gain: This changes the scale on the $y$-axis.
Timebase: This changes the scale on the x-axis.

## Multimeters:

A multimeter can be used as an ammeter, voltmeter and an ohm-meter.

If the multimeter displays the number 1 (on the far left hand side of the screen), then the measured variable is either off-the-scale or it is too high to be measured.
a.c. ammeters and voltmeters are calibrated to give the root mean square value.



In a.c. circuits, components MUST be able to withstand the peak current and peak voltage without being damaged.

The current in a resistor is unaffected by frequency.


## Problems



Q Calculate the peak current in a $2.3 \mathrm{k} \Omega$ resistor connected to a mains supply.

## Current:

Current is defined as the rate of flow of charge. It is the number of coulombs passing per second.

By re-arranging the formula:

$$
I=\frac{Q}{t}
$$

A current of 1 ampere is $\mathbf{1}$ coulomb per second (1 $\left.A=1 \mathrm{Cs}^{-1}\right)$

## Potential difference (voltage):

The potential difference between two points, is a measure of the work done in moving one coulomb of charge between the two points in a circuit e.g. across a lamp.
$\boldsymbol{W}=\boldsymbol{Q} \boldsymbol{x} \boldsymbol{V}, \quad \mathrm{W}=$ work done in joules (J)
$\mathrm{Q}=$ charge in coulombs (C)
$\mathrm{V}=$ potential difference in volts (V)

Remember: work is required to move electric charges through components of a circuit. The work done comes from the energy supplied to the charges as they pass through the battery/cell or any other source.

In a resistor, the work done becomes heat.

$$
V=\frac{W}{Q}
$$

From the re-arranged formula (above), the voltage is a measure of the energy given out per coulomb of charge.

1 volt is 1 joule per coulomb: $1 \mathrm{~V}=1 \mathrm{JC}^{-1}$.

## Electromotive force (e.m.f.):

The e.m.f. is the energy supplied by the cell to each coulomb of charge. The e.m.f. is measured in volts (or $\mathrm{JC}^{-1}$ )

## Resistance:

Ohm's Law holds for resistors at a constant temperature:

$$
R=\frac{V}{I}
$$

where $R$ is a constant, known as the resistance (in ohms, $\Omega$ ).

## Conservation of energy:

As charges pass through the source, the energy supplied per coulomb of charge must equal to the energy dissipated per coulomb of charge in that circuit.

In other words, the sum of the e.m.f.s round a circuit, is equal to the sum of the p.d.s round that circuit.

## Power:

Power is the rate of doing work. Power is the amount of work done per second.

$$
P=\frac{E}{t}
$$

where $P$ is the power measured in watts (W).

Power is related to potential difference (voltage), current and resistance by the following:

$$
\begin{gathered}
\boldsymbol{P}=\frac{E}{t}=\frac{Q V}{t}=\frac{I t V}{t}=\boldsymbol{I} \boldsymbol{V} \\
\boldsymbol{P}=I V=I(I R)=I^{2} \boldsymbol{R} \\
\boldsymbol{P}=I V=\left(\frac{V}{R}\right) V=\frac{\boldsymbol{V}^{2}}{\boldsymbol{R}}
\end{gathered}
$$



$$
V_{\text {supply }}=V_{1}+V_{2}+V_{3}
$$

The current, $l$, is the same in each resistor.
If $R$ is the total resistance,

$$
\begin{aligned}
I R & =I R_{1}+I R_{2}+I R_{3} \\
I R & =I\left(R_{1}+R_{2}+R_{3}\right) \\
\boldsymbol{R} & =\boldsymbol{R}_{\mathbf{1}}+\mathbf{R}_{\mathbf{2}}+\boldsymbol{R}_{\mathbf{3}}
\end{aligned}
$$

If the voltage supply is constant and $R$ increases then the

## Resistors in parallel:

From the circuit rules: the current in each branch adds up to the supply current: $I=I_{1}+I_{2}+I_{3}$

If $R$ is the total resistance,
$\frac{V_{\text {supply }}}{R}=\frac{V_{1}}{R_{1}}+\frac{V_{2}}{R_{2}}+\frac{V_{3}}{R_{3}}$
which is the same as

$$
V_{\text {supply }}\left(\frac{1}{R}\right)=V\left(\frac{1}{R_{1}}+\frac{1}{R_{2}}+\frac{1}{R_{3}}\right)
$$

Since $V_{\text {supply }}=V_{1}=V_{2}=V_{3}$
then $\frac{1}{R}=\frac{1}{R_{1}}+\frac{1}{R_{\mathbf{2}}}+\frac{1}{R_{\mathbf{3}}}$

When you add resistive branches in parallel the total resistance decreases.

The total resistance of a parallel network is always less than that of a resistor with the smallest resistance .

When branches are identical, i.e. all the resistors have the same resistance, the total resistance is worked out as
$R=\frac{\text { (resistance of one branch) }}{\text { number of branches }}$
$R=\frac{12}{3}=4 \Omega$

When branches are not all identical then use

$$
\begin{aligned}
& \frac{1}{R}=\frac{1}{R_{1}}+\frac{1}{R_{2}}+\frac{1}{R_{3}} \\
& \text { OR } \\
& R=\frac{R_{1} \times R_{2}}{R_{1}+R_{2}} \\
& R=\frac{6 \times 3}{6+3}=2 \Omega \\
& R=\frac{8 \times 2}{8+2}=1.6 \Omega
\end{aligned}
$$



Worked example: Calculate the total resistance in the circuit shown


Loop 1, loops around three $2 \Omega$ resistors, which are connected in series. The combined resistance in that loop is $6 \Omega$.

Loop 2, loops around a $2 \Omega$ resistor, which is connected across the $6 \Omega$ resistor.

The total resistance is given as

Method 1

$$
\begin{aligned}
\frac{1}{R} & =\frac{1}{R_{1}}+\frac{1}{R_{2}} \\
\frac{1}{R} & =\frac{1}{6}+\frac{1}{2} \\
\frac{1}{R} & =\frac{1+3}{6}=\frac{4}{6} \\
R & =1.5 \Omega
\end{aligned}
$$

Method 2
$R=\frac{6 \times 2}{6+2}$
$R=1.5 \Omega$

Potential divider calculations:
The equations used for potential divider calculations are -

$V_{1}=\left(\frac{R_{1}}{R_{1}+R_{2}}\right) V_{s} \quad$ and $\quad \frac{V_{1}}{R_{1}}=\frac{V_{2}}{R_{2}}$

You should know which equation to use for a given potential divider problem.

Remember: the bigger the component's resistance, the bigger the p.d. across that component (i.e. the bigger the share of the supply voltage).

Look at the example, next page, that involves two potential dividers in parallel.

## Problems



Q Calculate the total resistance in the circuit.


Q A variable resistor is slowly increased from $12 \Omega$ to $24 \Omega$.
Does the total power dissipated increase or decrease?
Justify your answer.

## Q

Calculate:
(a) the total resistance in the circuit
(b) the p.d. across each resistor
(c) the current in each resistor
(d) the power dissipated in each resistor
(e) the energy released, in each resistor, in 10 s .



## 1. Calculate the potential at point $A$.

The potential at point $A$ is the same as the voltage (p.d.) across $3000 \Omega$ resistor.
First step is to calculate the potential difference across 3000 $\Omega$ resistor.
Use the formula

$$
V_{1}=\left(\frac{R_{1}}{R_{1}+R_{2}}\right) V_{s}
$$

where $V_{1}$ is the voltage at point $A, R_{1}=3000 \Omega, R_{2}=1000 \Omega$ and $\mathrm{V}_{\mathrm{s}}=20 \mathrm{~V}$.

You should be able to work out that $\mathrm{V}_{1}=15 \mathrm{~V}$. So $\mathrm{V}_{\mathrm{A}}=15 \mathrm{~V}$.

## 2. Calculate the potential at point B.

This time $V_{1}$ is the voltage at point $B, R_{1}=6000 \Omega, R_{2}=4000 \Omega$ and $V_{s}=20 \mathrm{~V}$.
You should be able to work out that $\mathrm{V}_{1}=\mathbf{1 2} \mathrm{V}$. So $\mathrm{V}_{\mathrm{B}}=12 \mathrm{~V}$.
3. Calculate the potential difference between points $A$ and $B$.

$$
V_{A B}=V_{A}-V_{B}=15-12=3 V
$$

The above circuit is called a Wheatstone bridge. A Wheatstone bridge is made up of two potential dividers connected in parallel.

Generally, for any Wheatstone bridge, if the potential difference between points $A$ and $B\left(V_{A B}\right)$ is equal to zero volts, then the bridge balanced.

From the example above, the potential difference $\mathrm{V}_{\mathrm{AB}}$ is equal to 3 V and not 0 V . We would say the bridge is out-ofbalance.

The Wheatstone bridge circuit is very useful for monitoring changes in voltages between points A and B. A variable resistor, or a thermistor or LDR could be used to replace one of the four resistors. This special type of circuit would be used to show changes in temperature or light intensity etc.

## Electrical sources and internal resistance.

## Internal resistance:

Real cells and batteries have internal resistance.
An electrical source is equivalent to a source of e.m.f., E, with a resistor in series known as the internal resistance (r).


## e.m.f (E):

The e.m.f. is the energy supplied by the cell to each coulomb of charge. The e.m.f. is measured in volts (or JC ${ }^{-1}$ ).

## lost volts:

The p.d. across the internal resistance. The internal resistance is considered to be a constant.
$V_{\text {lost }}=I r$

## terminal potential difference:

The terminal potential difference is the voltage across the terminals of the supply. In a closed circuit, as shown above, the terminal potential difference is the same as the p.d. across the load resistor (R).

For a closed circuit: $\boldsymbol{V}_{\boldsymbol{t p d}}=\boldsymbol{I R}$ where $R$ is the load resistance, which can be a combination of resistors connected to the terminals of the source.
$V_{t p d}=E-V_{\text {lost }}$

## Worked example:

A 10 V cell has an internal resistance of $3 \Omega$. Calculate the terminal potential difference when the current drawn is 2 A?

$$
\begin{aligned}
& V_{t p d}=E-V_{l o s t} \\
& V_{t p d}=10-(2 \times 3) \\
& V_{t p d}=4 \mathrm{~V}
\end{aligned}
$$

Recap on expressions:
$E=V_{t p d}+V_{\text {lost }}$
$V_{t p d}=I R$
$V_{l o s t}=I r$
$I=\frac{E}{R+r}$
$r=\frac{E-V}{I}$

Q With reference to the above circuit, at the top of the page, explain what would happen to $V_{\text {lost }}$ and $V_{\text {tpd }}$ if the load resistance ( $R$ ) increases?

From this expression, as the circuit resistance $R$ increases, the current / decreases.

What does this mean?

* The lost volts ( $V_{\text {lost }}=I r$ ) decreases.
* The terminal potential difference, $\left(V_{t p d}=E-V_{\text {lost }}\right)$, increases.


## Graphs for working out internal resistance:

There are three graphs, which often appears in internal resistance questions. These are:

## 1. Lost volts versus current

The gradient of this graph defines the internal resistance.

2. Terminal potential difference versus current

From the equation
$E=V+I r$
so $V=(-r) I+E$
just like $y=m x+c$

So at zero current, the intercept on the p.d. axis ( $y$-axis) is the e.m.f.

The gradient is negative and is the internal resistance i.e. $\mathbf{r}=$-gradient.


The intercept on the current axis gives the short circuit current.

This is expressed as: $I_{\text {short }}=\frac{E}{r}$

3. Resistance versus $\frac{1}{\text { current }}$

$$
\begin{aligned}
E & =I R+I r \\
\frac{E}{I} & =R+r \\
\boldsymbol{R} & =\frac{\boldsymbol{E}}{\boldsymbol{I}}-\boldsymbol{r} \quad \text { just like } y=m x+c
\end{aligned}
$$

Here, the gradient = e.m.f., the intercept on the R-axis = $-\mathbf{r}$, the intercept on $\frac{1}{\text { current }}$ allows you to work out the short circuit current.

## Short circuit current:

A short circuit occurs when two or more terminals of a source of electrical energy are connected through a path of low resistance.
The circuit (on the right) has a wire of negligible resistance connected across the source. The maximum current available will take the route of low resistance (i.e. in the wire) and the wire itself will overheat. The size of the short circuit current is worked out as follows:

$$
I=\frac{E}{R+r} \begin{aligned}
& \begin{array}{l}
\text { Total load } \\
\text { resistance } \\
R=0 \Omega
\end{array}
\end{aligned} I=\frac{E}{0+r} \quad \boldsymbol{I}_{\text {short }}=\frac{\boldsymbol{E}}{\boldsymbol{r}} .
$$



## The heater element has a resistance of 1 ohm.

In this set-up, the element's output power is $\mathbf{2 5}$ watts.

## Calculate:

Q (i) the current in the heater
(ii) the terminal potential difference
(iii) the internal resistance of the supply.

Another 1 ohm heater element is connected in parallel
Q with the original heater element.
Calculate the terminal potential difference for this set-

up. (Remember: the internal resistance has not changed, so you cannot use the output power above for this problem).

Answers: (i) $5 \mathrm{~A} \quad$ (ii) $5 \mathrm{~V} \quad$ (iii) $0.1 \Omega$
extended: $R_{\text {parallel }}=0.5 \Omega, R+r=0.6 \Omega$,
method 1: $V=0.5 \times 5.5 / 0.6=4.58 V$
method 2: $I=E /(R+r)=5.5 / 0.6=9.17 \mathrm{~A}$,
$V_{t p d}=I R=9.17 \times 0.5=4.58 \mathrm{~V}$.

## Maximum power:

The maximum power in a load resistor, $R$, occurs when $R=r$.


## Voltmeters:

Voltmeters have nearly infinite resistance (actually it is around $1 \mathrm{M} \Omega$ ).
This means the total resistance in the circuit is very high.

The current in the circuit is negligible (close to 0 A ), so $\mathrm{Ir}=0$ volts (i.e. there are no lost volts). This means the voltmeter reads the e.m.f., which is 4 V .


## Capacitors

## Capacitance:

Capacitors store charge. Capacitance is measured in farads (F).
From the expression

$$
C=\frac{Q}{V}
$$

1 farad = 1 coulomb per volt ( $1 \mathrm{~F}=1 \mathrm{CV}^{-1}$ )

The charge stored, in the capacitor, is proportional to the p.d. between the plates.

The gradient of the line is a constant, which is the capacitance.


The area under the Q/V graph is the energy stored.

## Stored energy:

Work has to be done to charge a capacitor. The first electron placed on one plate repels the next. The more electrons already in place on the metal plate, the stronger the force of repulsion which has to be overcome to add more charge and so the greater the work that has to be done.
The work becomes energy stored in the electric field between the plates of the capacitor.

$$
E=\frac{1}{2} Q V \quad E=\frac{1}{2} Q V=\frac{1}{2} Q\left(\frac{Q}{C}\right)=\frac{1}{2} \frac{Q^{2}}{C} \quad E=\frac{1}{2} Q V=\frac{1}{2}(C V) V=\frac{1}{2} C V^{2}
$$

## Charging up a capacitor:

- The moment switch S is closed, $V_{R}=V_{s}$.
- Current reading is high ( $I=V_{s} \div R$ )
- As charge builds up on the plates, a potential difference is produced across the capacitor (i.e. $V_{C}$ increases).
- Since $\boldsymbol{V}_{\boldsymbol{R}}+\boldsymbol{V}_{\boldsymbol{C}}=\boldsymbol{V}_{\boldsymbol{s},} V_{R}$ decreases and $\boldsymbol{I}$ decreases as the capacitor charges up.
- Eventually, $V_{R}=0 \mathrm{~V}, I=0 \mathrm{~A}, V_{c}=V_{s}$.


The current/time and voltage/time graphs for an RC charging circuit are shown:


## Discharging a capacitor:

- The moment switch S is closed, $V_{R}=-V_{s}$ and $V_{c}=V_{s}$.
- Current reading is high ( $I=V_{s} \div R$ ), but negative in sign as the current is in opposite direction when charging.
- The capacitor is discharging through resistor R. The charge stored in the capacitor decreases and p.d. across it also decreases (i.e. $\mathrm{V}_{C}$ decays to 0 V )
- Since $\boldsymbol{V}_{\boldsymbol{R}}+\boldsymbol{V}_{\boldsymbol{C}}=\mathbf{0}, V_{R}$ decreases and $I$ decreases as the capacitor discharges.
- Eventually, $V_{R}=0 \mathrm{~V}, I=0 \mathrm{~A}, V_{c}=0 \mathrm{~V}$.


The current/time and voltage/time graphs for an RC discharging circuit are shown:




## Charging and discharging time:

The time to charge or discharge depends on the resistance and the capacitance.
Increasing the capacitance will cause the time for charging (or discharging) to increase. More charge will be stored.




Increasing the resistance will cause the time for charging (or discharging) to increase. The same charge will be stored.
The current starts smaller but takes longer to reduce to 0 A .



Q
A $500 \mu \mathrm{~F}$ capacitor is initially uncharged. Switch $S$ is now closed.
(a) State the p.d. across $\mathbf{2 k \Omega}$ after S is closed.
(b) State the p.d. across $500 \mu \mathrm{~F}$ after S is closed.
(c) Calculate the initial current.
(d) State the p.d. across $2 \mathrm{k} \Omega$ when the capacitor is fully charged.
(e) State the p.d. across $500 \mu \mathrm{~F}$ when the capacitor is fully charged.

(f) Calculate the charge stored on the fully charged capacitor.
(g) Calculate the energy stored on the fully charged capacitor.

Q
A $500 \mu \mathrm{~F}$ capacitor is charged and has a potential difference of 10 V . Switch S is now closed.
(a) Calculate the initial current after S is closed.
(b) While the capacitor is discharging, the voltage across it is 8 V .
Calculate the current in the circuit at that time.
(c) The voltage across the capacitor has now dropped to 2 V .


Calculate the charge and energy stored on the capacitor.

In a d.c. circuit, when a capacitor is fully charged, no more current flows. The capacitor blocks d.c.

In an a.c. circuit, the capacitor opposes the alternating current but does not block it completely. This opposition becomes less as the frequency increases. Because of this, the current increases as the frequency increases.


To do this as an experiment, you need an a.c. type voltmeter so that the voltage of the signal generator remains constant. The frequency of the signal generator is varied and the current is measured.


An $I_{c} / f$ graph is a straight line through the origin. This means current in the circuit is directly proportional to the frequency of the signal generator.

## Uses of capacitors:

- Capacitors block d.c. but allow a.c. to pass.
- Capacitors can be used to tune radio circuits.
- Capacitors store charge, which can be used to smooth the output of a rectified power supply. Rectification means changing a.c. to d.c.
- Capacitors are also used to direct high and low frequencies to the appropriate speakers in a hi-fi system.


## Conductors, insulators and semi-conductors.

## Materials:

Materials belong roughly to three categories depending on their electrical resistance: conductors (e.g. copper), insulators (e.g. polythene) and semiconductors (e.g. silicon).

## Energy levels:

The electrons, in each atom, are contained in energy levels.


The valence band contains electrons that are thought to be bound to the atom. The valence band is full in insulators and semiconductors.

When the atoms come together to form solids, the allowed energy levels are organised as energy bands, separated by gaps. There may be several of the allowed bands, but we'll consider the two uppermost bands: the valence band and conduction band.


> The conduction band contains allowed energy levels which are unoccupied in insulators and semiconductors, but partially filled in conductors.

Only partially filled bands allow conduction.

## Conductors:

Metals and semi-metals that have many free electrons.


In conductors, the valence band is full and there are some electrons in the conduction band. The conduction band is not completely full and this allows the electrons to move and therefore conduct.
A conductor conducts well at room temperature.

## Insulators:

Electrons are all bonded and there are no or few free electrons.


In an insulator, the highest occupied band (i.e. the valence band) is full. The gap between the valence band and conduction band is large.

At room temperature, there is not enough energy to move electrons from the valence band to the conduction band. This means there are no or very few electrons in the conduction band that would be able to contribute to conduction.

## Semiconductors:

Such materials have a resistance between good conductors and good insulators.


In a semiconductor, the gap between the two bands is smaller than that of an insulator. The valence band is full.

At room temperature, there is enough energy to move electrons from the valence band to conduction band. This means some conduction can take place.
Increasing the temperature of a semiconductor will increase the conductivity of a semiconductor.

During manufacturing, the conductivity of semiconductors can be controlled by means of doping. Doping means adding a very few "impurity" atoms to a pure semiconductor. This reduces its resistance. As a result, there are two types of semiconductors that can be produced from doping: $\mathbf{p}$-type and $\mathbf{n}$-type.

N-type semiconductor:


The majority of charge carriers, in n-type semiconductors, are negative (electrons).
Group $V$ doping agents result in n-type extrinsic semiconductors, which contain extra electrons.

P-type semiconductor:

pure semiconductor valence 4

## $+$

 valence 3


P-type semiconductor

The majority of charge carriers, in p-type semiconductors, are positive ("holes").
Group III doping agents result in p-type extrinsic semiconductors, which contain extra holes.

- Semiconductors may be doped with impurities that add either extra electrons or holes to the lattice.
- Doping of semiconductors can significantly reduce the width of the gap between the conduction and valence band.
- The energy band gap in semiconductors is small enough that thermal excitation is sufficient for significant numbers of electrons to be able to move up from the valence to the conduction band.
- Semiconductors allow conduction by means of negative charge carriers (electrons) or positive charge carriers ("holes").


## Fermi level:

At this level, the Fermi level represents a point where it is equally likely that an electron is, or is not, present. The Fermi level (i.e. the energy of the Fermi level) must remain constant through any semiconductor device if there is no applied field.


P-type silicon:


The Fermi level gets closer to the valence band since electrons have been removed.

A p-n junction diode is made from a crystal of semiconductor material, half of which is p-type and half n-type.

| Conduction <br> band | Conduction <br> band |
| :---: | :---: |
| Fermi level | Fermi level |

Band model of a non-biased p-n junction:


The region where the conduction and valence bands bend is called the depletion layer.

The Fermi level is flat in the depletion layer.

The slope in the conduction band acts as a potential barrier as it would require work, of value $e V_{D}$, to get electrons to move against the barrier.

When the two types are interfaced, some electrons and holes diffuse across the border and combine.


Since the electrons and holes have combined, there are no free charges carriers around the junction. This is called the depletion layer.


As shown above, there are fixed charged atoms in the depletion layer that stop any further movement of charge carriers across the junction. This is equivalent to having a voltage (p.d.) across the junction, which opposes further movement of electrons (from n-type to p-type) and holes ( n type to $p$-type). (See the band model to the left.)

Applying a voltage to a semiconductor device is called biasing.

This is very useful, as this represents a diode. A diode is a device which conducts electricity in one direction much more than in the other.

Symbol:


## Forward bias:



- When the diode is forward biased, current flows.
- The depletion layer is reduced.
- Electrons flow into the depletion layer from the n-type then into the p-type.
- Holes flow into the depletion layer from the p-type then into the n-type.



The band model of forward biased p-n diode (above), shows that the energy difference between the conduction band on the p-type side and on the n-type side is smaller. This allows current to flow far more easily.

## Reverse bias:



- When the diode is reversed biased, almost no current flows.
- The depletion layer widens.
- Electrons in n-type are pulled by positive supply.
- Holes in p-type are pulled by negative supply.
- There is a wider area with no free charge carriers, a wider depletion layer and the diode does not conduct.


The band model of reversed biased p-n diode (above), shows that the energy difference between the conduction band on the p-type side and on the n-type side is bigger. This makes it difficult for current to flow through it.

## LED:

An LED is a forward biased p-n junction diode. When the LED conducts, the emission of light is caused by the electrons combining with holes to give out energy as photons of light. Each electron-hole recombination gives one photon out.


## The photodiode:

Electron-hole pairs are produced when light falls on the junction (depletion layer). Each photon gives up its energy and produces one electron-hole pair.


Solar cells:

The photodiode is operating in photovoltaic mode. This means the photodiode can be used to supply power to a load. When the photon is incident on the junction, its energy is absorbed, freeing electrons and creating electron-hole pairs. This creates a voltage. The more light, the more photons are absorbed and
 the higher the voltage.

